COMPATIBLE IBM PC/AT™ CPU CARD
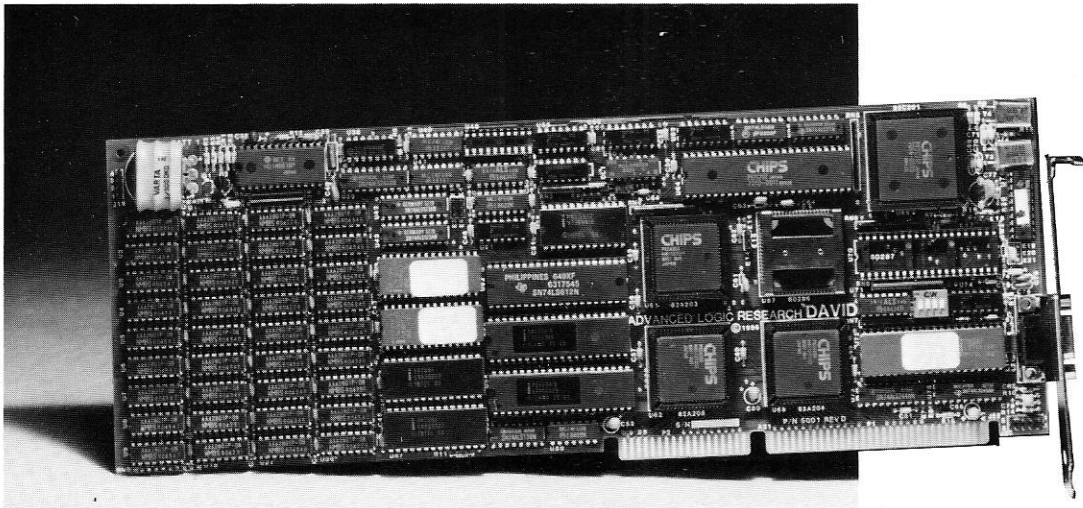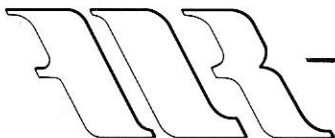
GENERAL DESCRIPTION

ALR announces a new concept for IBM PC/AT packaging. The compact DAVID CARD contains all the functions of the full-sized motherboard except for expansion slots. The DAVID CARD, and other system cards, plugs into an IBM PC/AT compatible backplane, which is available with 8 to 13 expansion slots.

FEATURES

☐ *Fully hardware/software compatible with IBM PC/AT.*
☐ *Phoenix BIOS.*
☐ *ON-BOARD 1MB of memory with parity.*
☐ *12 MHz, 80286 CPU (8 or 10 MHz optional).*
☐ *Keyboard selectable 8 MHz speed.*
☐ *24-bit address and 16-bit data paths.*
☐ *7 DMA channels, speaker output port, and keyboard port.*
☐ *15 level interrupt.*
☐ *3 programmable timers.*
☐ *Clock/calendar with battery backup.*
☐ *Low power consumption via CMOS technology.*
☐ *Backplanes available with 8 or 13 slots. The DAVID CARD occupies one of the slots.*
☐ *Optional custom design for industrial OEM.*
☐ *Designed for serviceability and reliability.*

Advanced Logic Research, Inc.

COMPATIBLE IBM PC/AT CPU CARD

SPECIFICATIONS

Dimensions:	*13.13" × 4.5" multilayered card*
Microprocessor:	*16-bit, 80286, 12 MHz.*
Coprocessor (Option):	*80287-10*
RAM Memory:	*0, 512K, 640K or 1MB (switch selectable). Supports combinations of 64K and 256K DRAMS.*
EPROM:	*32KB (Phoenix BIOS)*
Basic ROM:	*None*
Address Bus:	*24-bit (support 0-16MB addresses)*
Data Bus:	*16-bit*
DMA Channels:	*7*
Interrupts:	*15*
Timers:	*3 programmable timers*
Keyboard Interface:	*DB9 female connector interfaces to rear bracket or 10-pin post stack connector on DAVID CARD.*
Speaker Interface:	*1/2 watt audio output circuit with 3-pin post connector.*
Vibration (Vertical):	
Operating:	*2.5G RMS, 5-500Hz*
Storage:	*3.0G RMS, 5-500Hz*
Power Requirements:	*+5VDC @ 5 amp (maximum)*
Environment:	
Operating Temperature:	*0 - 55 degrees C.*
Humidity:	*0 - 95% noncondensing*
Backplane Option:	*13-slot or 8-slot provides 12 or 7 expansion slots after DAVID CARD installation. Optional custom design for industrial OEM.*
Installation:	*Plugs directly into ALR backplane described above.*